

L Number	Hits	Search Text	DB	Time stamp
-	7	revision\$4 same rom same ((chip with computer) microprocessor) same firmware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:09
-	19	revision\$4 same ((chip with computer) microprocessor) same firmware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/03 09:10
-	12	(revision\$4 same ((chip with computer) microprocessor) same firmware) not (revision\$4 same rom same ((chip with computer) microprocessor) same firmware)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/03 09:13
-	111	firmware with patch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/03 09:14
-	4	(firmware with patch\$3) same interrupt	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/03 09:17
-	6	(firmware with patch\$3) and (revision\$4 same interrupt)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/03 09:18
-	603	vector same temporary same stor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:10
-	43	vector same temporary same stor\$3 same interrupt	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:10
-	9	(interrupt adj controller) same vector same temporary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:19
-	482	(interrupt adj controller) same vector	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:20
-	15	((interrupt adj controller) same vector).ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:21
-	163	(vector adj table) same (vector with register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:51

-	0	(vector adj table) same (vector with register) same (temporary with sav\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:52
-	17	(vector adj table) same (vector with register) same (temporary sav\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 09:52